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7/19/00
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Albrecht Mayer

Applic. No. : 09/164,123

Filed : September 30, 1998

Title : Method for Assembling Integrated Circuits
With Protection Of The Circuits Against
Electrostatic Discharge, And Arrangement Of
Integrated Circuits With Protection Against
Electrostatic Discharge

Examiner : Kurt M. Eaton

Group Art Unit : 2823

ASSOCIATE POWER OF ATTORNEY

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Please recognize MARK P. WEICHSELBAUM (Reg. No. 43,248) as my
associate in the matter of the above-identified application,
with full powers. Please continue addressing all
communications to the following address:

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Respectfully submitted,

For Applicant

Date: July 12, 2000

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